



Inventor:

Baretich et al.

Examiner:

Vu, B,

Serial No.:

10/068,287

Group Art Unit:

2838

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PATENT

Title of Invention:

Method and Apparatus for Electronic Power Control

Seattle, Washington 98169
January 27, 2003

TO THE COMMISSIONER FOR PATENTS

Washington, D.C. 20231

AMENDMENT AND RESPONSE TO OFFICE ACTION

Applicant acknowledges receipt of an Office Action dated 09/25/2002. Also filed simultaneously herewith is a Petition and Fee for an Extension of One Month's Time within which to file this Amendment and Response. Applicant responds to the Examiner's concerns as follows:

AMENDMENT

In the Claims:

Please add new Claim 5 as follows:

5. An HMI ballast comprising a split mode operation wherein the ballast operates as an inverter part of the time and operates as a direct converter part of the time; the ballast employing a plurality of switch modes to successively modulate one or more of a plurality of switches and diodes; whereby line voltage and current are controlled to provide a substantially square wave voltage and current output.

CERTIFICATE OF MAILING (37 CFR 1.8A)

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner of Patents and Trademarks, Washington, D. C. 20231.

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Patrick Dwyer